Book Chapters and Extended Proceedings

[B11] T. Costa, V. Gaudet, E.R. Vrscay, and Z. Wang, "Variation of Perceived Colour Difference Under Different Surround Luminance," in *Lecture Notes in Computer Science 12131: Image Analysis and Recognition*, A. Campilho, F. Karray, and Z. Wang, eds., Springer, pp. 56-60, 2020.

[B10] V.C. Gaudet, W.J. Gross, and K.C. Smith, "Chapter 1: History of Stochastic Computing," in *Stochastic Computing: Techniques and Applications*, W.J. Gross and V.C. Gaudet, eds., Springer, pp. 1-11, 2019.

[B9] F. Leduc-Primeau, S. Hemati, V.C. Gaudet, and W.J. Gross, "Chapter 11: Stochastic Decoding, Algorithms and Circuits," in *Stochastic Computing: Techniques and Applications*, W.J. Gross and V.C. Gaudet, eds., Springer, pp. 201-215, 2019.

[B8] F. Leduc-Primeau, W. Gross, and V. Gaudet, "Stochastic Decoders for LDPC Codes," in *Advanced Hardware Design for Error Correcting Codes*, P. Coussy and C. Chavet, eds., Springer, pp. 105-128, 2015.

[B7] V. Gaudet, "Low-Power Design Techniques for State-of-the-Art CMOS Technologies," in *Recent Progress in the Boolean Domain*, B. Steinbach, ed., Cambridge Scholars Publishing, pp. 189-212, 2014.

[B6] A. Razib, S. Dick, and V. Gaudet, "Log-Domain Arithmetic for High-Speed Fuzzy Control on a Field-Programmable Gate Array," in *Soft Computing: State of the Art Theory and Novel Applications*, R. Yager, A. Abassov, M. Reformat, and S. Shahbazova, eds., Springer-Verlag, pp. 269-287, 2012.

[B5] V. Gaudet, "Low Power LDPC Decoding by Exploiting the Fault-Tolerance of the Sum-Product Algorithm," in *Contemporary Mathematics (CONM) - Proceedings of the Conference on Error-Correcting Codes, Cryptography and Finite Geometries*, A. Bruen and D. Wehlau, eds., American Mathematical Society, pp. 165-172, 2010.

[B4] R. Długosz, V. Gaudet, and R. Wojtyna, "Gilbert-Multiplier-Based 1-D and 2-D Analog FIR Filters for Medical Diagnostics," in *Computers in Medical Activity*, AISC 65, E. Kacki, M. Rudnicki, and J. Stempczynska, eds., Springer-Verlag, pp. 85-99, 2009.

[B3] Y. Wang, A. Niknejad, V. Gaudet, and K. Iniewski, "CMOS IR-UWB Transceiver System Design for Contact-Less Chip Testing Applications," Chapter 5, *Circuits and Systems for Future Generations of Wireless Communications*, A. Tasic, W. Serdijn, L. Larson, G. Setti, eds., Springer, pp. 85-119, 2009.

[B2] S. Kasnavi, P. Berube, V. Gaudet, and J.N. Amaral, "A Multizone Pipelined Cache for IP Routing," *Springer-Verlag Lecture Notes in Computer Science*, Vol. 3462 / 2005, pp. 574-585, Apr. 2005.

[B1] V. Gaudet, "Genetic Programming of Logic-Based Neural Networks," *Genetic Algorithms*