Invited Lectures and Academic Visits

[L19] V. Gaudet, “The Renaissance of Stochastic Computing,” *Reed-Muller Workshop*, Fredericton, NB, May 2019.

[L18] V. Gaudet, “The Renaissance of Stochastic Computing,” *University of British Columbia*, June 2018.

[L17] V. Gaudet, “The Renaissance of Stochastic Computing,” *Simon Fraser University*, May 2018.

[L16] V. Gaudet, “The Renaissance of Stochastic Computing,” *University of Miami*, October 2016.

[L15] V. Gaudet, "Energy-Efficient LDPC Decoding," *National Taiwan University*, Taipei, Taiwan, July 2009.

[L14] V. Gaudet, "Energy-Efficient Decoding Using Analog VLSI Techniques," *McGill University*, Montreal, QC, November 2008.

[L13] V. Gaudet, "Energy-Efficient LDPC Decoding: Code Design and Algorithms," *Rensselaer Polytechnic Institute*, Troy, NY, November 2008.

[L12] V. Gaudet, "Energy-Efficient Decoding Using Analog VLSI Techniques," *Northeastern University*, Boston, MA, November 2008.

[L11] V. Gaudet, "Energy-Efficient LDPC Decoding: Code Design and Algorithms," *McGill University*, Montreal, QC, May 2008.

[L10] V. Gaudet, "Energy Efficient Information Processing Using Analog VLSI Techniques," *Tokyo Institute of Technology*, Tokyo, Japan, August 2007.

[L9] V. Gaudet, "Energy-Efficient Decoding Using Analog VLSI Techniques," Nortel Institute, *University of Waterloo*, Waterloo, ON, March 2007.

[L8] V. Gaudet, "Low Density Parity Check Codes and Decoding Architectures," *Tohoku University*, Sendai, Japan, September 2006.

[L7] V. Gaudet, "Analog Decoding: State of the Art and Design Challenges," ATIPS Laboratory, *University of Calgary*, November 2005.

[L6] V. Gaudet, "Field-Programmable Analog Arrays: An Introductory Survey," *Tohoku University*, Sendai, Japan, September 2004.

[L5] Visiting Professor, *Université de Bretagne Sud*, Lorient, France, July 2004.

[L4] V. Gaudet, "Error-Control Coding in Next-Generation Digital Communication Standards,"

University of Bern, Bern, Switzerland, April 2004.

[L3] V. Gaudet, "A Tutorial on Analog Iterative Decoding," *Texas A&M University*, College Station, TX, December 2002.

[L2] V. Gaudet, W. Gross, and G. Gulak, "Une architecture VLSI pour la détection souple de signaux PR4," *Université de Bretagne Sud*, Lorient, France, June 2001.

[L1] V. Gaudet, W. Gross, and G. Gulak, "Une architecture VLSI pour la détection souple de signaux PR4," *ENST-Bretagne*, Brest, France, June 2001.